

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I declare that: My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: GM CELL BASED CONTROL LOOPS the specification of which is attached hereto.

I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56. I claim foreign priority benefits under Title 35, United States Code, Section 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Country	Application No.	Date of Filing	Priority Claimed Under 35 USC 119

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below:

Application No.	Filing Date
60/148,379	Aug. 11, 1999
60/148,417	Aug 11, 1999
60/148,418	Aug 11, 1999

I claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Application No.	Date of Filing	Status

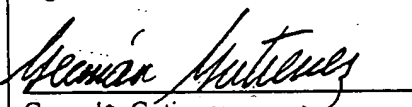
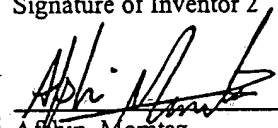
POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Babak S. Sani, Reg. No. 37,495

Send Correspondence to: Babak S. Sani TOWNSEND and TOWNSEND and CREW LLP Two Embarcadero Center, 8 th Floor San Francisco, California 94111-3834	Direct Telephone Calls to: (Name, Reg. No., Telephone No.) Name: Babak S. Sani Reg. No.: 37,495 Telephone: 415-576-0200
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Full Name of Inventor 1:	Last Name: GUTIERREZ	First Name: GERMAN	Middle Name or Initial: G	
Residence & Citizenship:	City: Carlsbad	State/Foreign Country: CA	Country of Citizenship: United States of America	
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Full Name of Inventor 2:	Last Name: MOMTAZ	First Name: AFSHIN	Middle Name or Initial:	
Residence & Citizenship:	City: Irvine	State/Foreign Country: CA	Country of Citizenship: United States of America	
Post Office Address:	Post Office Address: 4061 Germainder Way	City: Irvine	State/Country: CA	Postal Code: 92612

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signature of Inventor 1  German Gutierrez	Signature of Inventor 2  Afshin Momtaz	Signature of Inventor 3 _____
Date 3/23/00	Date 3/28/2000	Date

50769/B600

PATENT

ART UNIT

2634

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No. 30/DBP/B600

SUBSTITUTION OF ATTORNEY WITH
CHANGE OF ADDRESS FOR CORRESPONDENCE
BY ASSIGNEECommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Commissioner:

Broadcom Corporation, assignee of the entire interest in and to each U.S. patent application, identified on Exhibit A attached, under an Assignment recorded in the U.S. Patent and Trademark Office, hereby revokes all previous Powers of Attorney and appoints:

D. Bruce Prout	(20,958)	Harold E. Wurst	(22,183)	Jun-Young E. Jeon	(43,693)
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LeRoy T. Rahn	(20,356)	Richard A. Wallen	(22,671)	Stephen D. Burbach	(40,285)
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Mark Garscia	(31,953)	Raymond R. Tabandeh	(43,945)	Richard J. Paciulan	(28,248)
Syed A. Hasan	(41,057)	Cynthia A. Bonner	(44,548)	Richard A. Clegg	(33,485)

all members or associates of or of counsel to the firm CHRISTIE, PARKER & HALE, LLP, telephone (626) 795-9900, as principal attorneys with power to appoint associate attorneys, to prosecute this application and any subsequent application based on the disclosure of this application, and to transact all business in the Patent and Trademark Office connected with this application and any subsequent application.

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on: 8/5/03
(Date of Deposit) [Signature]

B600:30/DBP

The authority under this Power of Attorney of each person named above shall automatically terminate and be revoked upon such person ceasing to be a member or associate of or of counsel to that law firm.

Please address all correspondence to **CHRISTIE, PARKER & HALE, LLP**, P.O. Box 7068, Pasadena, California 91109-7068. Customer Number: 23363

Broadeom Corporation

Date:

8/4/3

By

Print Name: Dee Henderson
Title: Intellectual Property Portfolio
Manager

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CPH/BP NO.	APPLICATION NO.	FILING DATE	TITLE
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50763/BP1816	09/861,143	May 17, 2001	Layout Technique for C3MOS Inductive Broadbanding
50764/BP1814	09/956,374	September 17, 2001	Methods and Circuitry for Implementing First-In First-Out Structure
50766/BP2447	10/014,036	December 10, 2001	Wide-Band Tapered-Slot Antenna for RF Testing
50765/BP2362	10/209,293	July 30, 2002	Jitter Suppression Techniques for Laser Driver Circuits
50767/BP-NPC16 (BP1659CON)	10/263,259	October 1, 2002	Regenerative Signal Level Converter
50768/BP1656CON2	10/286,597	November 1, 2002	Overflow Detector for FIFO
50769/BP1647	10/318,586	December 13, 2002	GM Cell Based Control Loops
50814/BP1659	6,559,685	May 6, 2003	Regenerative Signal Level Converter

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